



PTO/SB/21 (09-04)

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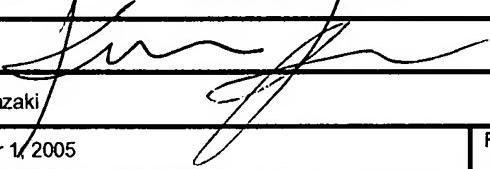
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Total Number of Pages in This Submission

Application Number	10/812,525
Filing Date	March 29, 2004
First Named Inventor	Stephen M. Trimberger
Art Unit	2819
Examiner Name	Vibol Tan
Attorney Docket Number	X-1006-2C US

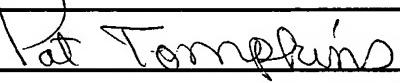
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### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	XILINX, INC. CUSTOMER NO/ 24309		
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Printed name	Kim Kanzaki		
Date	December 1, 2005	Reg. No.	37,652

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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X-1006-2C US  
10/812,525

PATENT  
Conf. No.: 3296

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger  
Assignee: Xilinx, Inc.  
Title: A Programmable Logic Device With Time-Multiplexed Interconnect  
Serial No.: 10/812,525 File Date: 3/29/2004  
Examiner: Vibol Tan Art Unit: 2819  
Docket No.: X-1006-2C US Conf. No.: 3296

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RESPONSE TO THIRD OFFICE ACTION

Dear Sir:

In response to the Second Office Action mailed from the Patent Office on September 02, 2005, please replace/substitute the following claims as indicated.

**Listing of the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 5 of this paper.